



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Leonard Forbes

**Serial No.:** 09/943,968

**Filed:** August 30, 2001

**For:** TECHNIQUE TO  
SIMULTANEOUSLY DISTRIBUTE  
CLOCK SIGNALS AND DATA ON  
INTEGRATED CIRCUITS, INTERPOSERS,  
AND CIRCUIT BOARDS

**Confirmation No.:** 1619

**Examiner:** D. Ha

**Group Art Unit:** 2634

**Attorney Docket No.:** 2269-4720US  
(00-1120.00/US)

**NOTICE OF EXPRESS MAILING**

Express Mail Mailing Label Number: EL994845975US

Date of Deposit with USPS: September 6, 2005

Person making Deposit: Steve Wong

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Mail Stop Issue Fee  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is filed in response to the Notice of Allowance mailed November, 5, 2004. This communication sets forth Applicants' comments, pursuant to 37 C.F.R. §1.104(e), on the Examiner's Statement of Allowable Subject Matter accompanying the Notice of Allowance.

In the Statement of Allowable Subject Matter, the Examiner indicates:

[The present invention relates to simultaneous transmission of digital data and clock signals to eliminate skewing of the data and clock signals with respect to each others. Upon further consideration, prior art of record, taking individually or

collectively, fails to fairly teach such method and apparatus including “a phase shift keying circuit for performing phase shift keying of said digital signal onto said clock signal to create a lead output signal and a lag output signal; a digital signal demodulator for demodulating said lead output signal and said lag output signal from said phase shift keying circuit to retrieve said digital signal; and a clock signal demodulator for demodulating said lead output signal and said lag output signal in conjunction with said digital signal to retrieve said clock signal” in independent claim 1; “a lead phase shift network receiving said sinusoidal oscillator signal and said digital signal for producing a positive phase shift in a lead output signal when said digital signal has a logical high value; and a lag phase shift network receiving said sinusoidal oscillator signal and a complementary digital signal for producing a negative phase shift in a lag output signal when said complementary digital signal has a logical high value” in independent claim 24; “a differential amplifier for comparing said lead signal and said lag signal to produce a differential output signal; a transistor amplifier circuit for receiving said differential output signal and producing a transistor signal dependent upon the square of an AC component of said differential output signal; an RC low pass filter for receiving said transistor signal and outputting a voltage signal; a voltage comparing circuit for receiving said voltage signal and a voltage reference signal to produce a demodulated digital data signal” in independent claim 32; and “generating a sinusoidal oscillator signal and a complementary digital signal; creating a lead output signal from said sinusoidal oscillator signal and said digital signal; creating a lag output signal from said sinusoidal oscillator signal and said complementary digital signal; transmitting said lead output signal and said lag output signal on matched interconnection lines to a vicinity of a clocked element; demodulating said lead output signal and said lag output signal for retrieving said digital signal; demodulating said lead output signal and said lag output signal using said digital signal for retrieving a clock signal; and inputting said digital signal and said clock signal into said clocked element” in independent claim 38 (claims 2-23, 25-31, 33-37, 39-48 depend therefrom). Thus, claims 1-48 are found to be novel and unobvious over prior art of record.]

Applicants concur with the reasons as stated by the Examiner insofar as they comprise a summary, which is exemplary and not limiting. However, the scope of the claims is based on the actual language of the claims and equivalents thereof, and not on a paraphrase or summary of the claim language.

The Independent claims as allowed recite features and methodology in addition to, and in different language than, those described in the Statement of Allowable Subject Matter. Furthermore, the dependent claims recite elements in addition to those of the independent claims, which are also not reflected in the Statement of Allowable Subject Matter. Such additional

elements, in combination with those of the independent claims from which each claim depends, provide additional reasons for patentability. Accordingly, the scope of the claims must be determined from the literal language of each as a whole, as well as all equivalents thereof.

Therefore, to the extent that the Examiner's reasons for allowance as stated are not relevant to, or wholly encompassing of, a particular claim, independent or dependent, Applicants assume that (pursuant to 37 C.F.R. §1.104(e)) the Examiner has determined that the record of the prosecution as a whole of the application makes clear the reasons for allowing those claims. Further, it appears, pursuant to M.P.E.P. 1302.14, that the Examiner's Statements of Allowable Subject Matter are not intended to encompass all of the reasons for allowance.

Respectfully submitted,



Jeff M. Michelsen  
Registration No. 50,978  
Attorney for Applicant  
TRASKBRITT  
P.O. Box 2550  
Salt Lake City, Utah 84110-2550  
Telephone: 801-532-1922

Date: September 6, 2005  
JMM/dlm:eg

Document in ProLaw



0917105

B / In  
✓

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Leonard Forbes

**Serial No.:** 09/943,968

**Filed:** August 30, 2001

**For:** TECHNIQUE TO  
SIMULTANEOUSLY DISTRIBUTE  
CLOCK SIGNALS AND DATA ON  
INTEGRATED CIRCUITS, INTERPOSERS,  
AND CIRCUIT BOARDS

**Confirmation No.:** 1619

**Examiner:** D. Ha

**Group Art Unit:** 2634

**Attorney Docket No.:** 2269-4720US  
(00-1120.00/US)

**Notice of Allowance Mailed:**

June 3, 2005

**NOTICE OF EXPRESS MAILING**

Express Mail Mailing Label Number: EL994845975US

Date of Deposit with USPS: September 6, 2005

Person making Deposit: Steve Wong

**TRANSMITTAL LETTER**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant submits herewith Part B - Fee(s) Transmittal for the above-captioned application and a check in the amount of \$1,715.00 in payment therefor plus five (5) copies of the patent when issued.

Also enclosed are Request for Corrected Filing Receipt with attached copy of Official Filing Receipt with annotations in red; Amendment Pursuant to 37 C.F.R. § 1.312(a); Comments on Statement of Reasons for Allowance; and Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees.

Applicant understands that no additional fees are required. However, if the Office determines that any comparison fees or other additional fees are required, the Commissioner is authorized to charge any such fees to TraskBritt Deposit Account No. 20-1469. A copy of this Transmittal Letter is enclosed for deposit account charging purposes.

Respectfully submitted,



Jeff M. Michelsen  
Registration No. 50,978  
Attorney for Applicant  
TRASKBRITT  
P.O. Box 2550  
Salt Lake City, Utah 84110-2550  
Telephone: 801-532-1922

Date: September 6, 2005

JMM/dlm:eg

Enclosures: Part B - Issue Fee Transmittal

Check No. 22093 in the amount of \$1,715.00

Copy of Transmittal Letter

Request for Corrected Filing Receipt (2 pages) with attached copy of Official Filing Receipt with annotations in red (1 page)

Amendment Pursuant to 37 C.F.R. § 1.312(a) (20 pages)

Comments on Statement of Reasons for Allowance (3 pages)

Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees (2 pages)

Document in ProLaw